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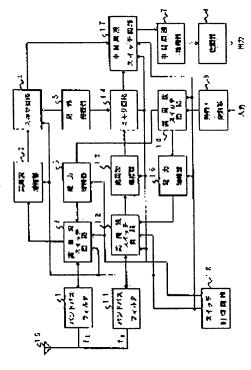
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# (54) RADIO TRANSMITTER-RECEIVER

# (57)Abstract:

PURPOSE: To select a transmission frequency band and a reception frequency band and to perform highly sensitive reception by turning off a reception part which does not perform reception and a transmission part which does not perform transmission.

CONSTITUTION: During fb frequency band reception and fa frequency band transmission, a high frequency switching circuit 2 is switched to the side of a high frequency amplifier 3, the high frequency switching circuit 12 is switched to the side of a power amplifier 16 and an intermediate switching circuit 17 is switched to the side of a mixer circuit 4. Thus, the reception signals of an fb frequency band passed through a BPF 1 passes through the circuit 2, lead to the side of the amplifier 3, passes through the intermediate frequency switching circuit 17 and demodulated in a demodulator 8. In the meantime, output modulation wave signals from an oscillator/modulator 9 are



transmitted through the BPF 11 and an antenna 15. Also, during fb frequency band transmission and fa frequency band reception, the circuit 2 is switched to the side of the high frequency amplifier 13, the high frequency switching circuit 12 is switched to the side of the power amplifier 16 and the circuit 17 is switched to the side of the mixer circuit 14.

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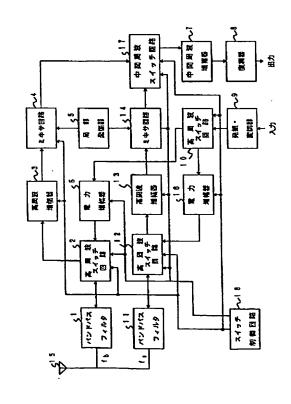
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#### (54) 【発明の名称】無線送受信機

#### (57) 【要約】

【目的】 小型でかつ安価な構成によって、送信周波数 帯域と受信周波数帯域を選択できかつ高感度の受信が可 能な無線送受信機を提供する。

【構成】 バンドパスフィルタ1を介した受信信号を高 周波スイッチ回路2を通して受ける第1受信部を構成す る高周波増幅器3およびミキサ回路4と、バンドパスフ ィルタ11を介した受信信号を高周波スイッチ回路12 を通して受ける第2受信部を構成する高周波増幅器13 およびミキサ回路14と、高周波スイッチ回路2を介し て送信出力を送出する電力増幅器6と、高周波スイッチ 回路2を介して送信出力を送出する電力増幅器16と、 第1受信部の出力または第2の受信部の出力を選択する 中間周波スイッチ回路17とを備え、複信通信のときに 受信しない方の受信部および送信出力を送出しない方の 電力増幅器を非動作状態に制御するようにした。



#### 【特許請求の範囲】

【請求項1】 異なる周波数帯で送信と受信とが同時に 行える複信方式の無線送受信機において、

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アンテナに接続された通過周波数帯域が異なる第1およ び第2のパンドパスフィルタと、

第1のパンドパスフィルタに接続された第1の高周波ス イッチ回路および第2のパンドパスフィルタに接続され た第2の高周波スイッチ回路と、

第1の髙周波スイッチ回路を通した受信信号を増幅して 中間周波数に変換する第1の受信部および第1の高周波 10 スイッチ回路を通した受信信号を増幅して中間周波数に 変換する第2の受信部と、

第1の高周波スイッチ回路を通して第1のバンドパスフ ィルタの通過周波数帯域の周波数の送信をする第1の送 信部および第2の髙周波スイッチ回路を通して第2のバ ンドパスフィルタの通過周波数帯域の周波数の送信をす る第2の送信部と、

第1および第2の受信部からの出力の一方を選択して中 間周波増幅器へ出力する選択手段と、

第1のパンドパスフィルタの通過周波数帯域の周波数の 受信を行うと共に第2のバンドパスフィルタの通過周波 数帯域の周波数による送信を行うとき、第1の受信部を 動作状態に、第2の受信部を非動作状態に、第1の送信 部を非動作状態に、第2の送信部を動作状態に、第1の 高周波スイッチ回路を第1の受信部へ受信信号を導く側 に、第2の髙周波スイッチ回路を第2の送信部の出力通 過側におよび選択手段を第1の受信部からの出力選択側 に制御し、かつ第1のバンドパスフィルタの通過周波数 帯域の周波数による送信を行うと共に第2のバンドパス フィルタの通過周波数帯域の周波数の受信を行うとき、 第1の受信部を非動作状態に、第2の受信部を動作状態 に、第1の送信部を動作状態に、第2の送信部を非動作 状態に、第1の高周波スイッチ回路を第1の送信部の出 力通過側に、第2の高周波スイッチ回路を第2の受信部 へ受信信号を導く側におよび選択手段を第2の受信部か らの出力選択側に制御する制御手段と、

を備えたことを特徴とする無線送受信機。

【請求項2】 請求項1記載の無線送受信機において、 制御手段は、第1のバンドパスフィルタからの受信信号 を第1の受信部に導く第1のダイオードスイッチ回路 と、第1の送信部の出力を第1のパンドパスフィルタに 導く第2のダイオードスイッチ回路と、第2のパンドパ スフィルタからの受信信号を第2の受信部に導く第3の ダイオードスイッチ回路と、第2の送信部の出力を第2 のパンドパスフィルタへ導く第4のダイオードスイッチ 回路と、第1の受信部の出力を選択する第5のダイオー ドスイッチ回路と、第2の受信部の出力を選択する第6 のダイオードスイッチ回路と、第1のパンドパスフィル タの通過周波数帯域の周波数の受信を行うと共に第2の パンドパスフィルタの通過周波数帯域の周波数による送 50

信を行う場合に第1、第4および第5のダイオードスイ ッチ回路をオン状態に制御すると共に第1の受信部およ び第2の送信部に電源を供給する第1のトランジスタス イッチ手段と、第1のパンドパスフィルタの通過周波数 帯域の周波数による送信を行うと共に第2のバンドパス フィルタの通過周波数帯域の周波数の受信を行う場合に 第2、第3および第6のダイオードスイッチ回路をオン 状態に制御すると共に第2の受信部および第1の送信部 に電源を供給する第1のトランジスタスイッチ手段とを 備えたことを特徴とする無線送受信機。 -

#### 【発明の詳細な説明】

[0001]

【産業上の利用分野】本発明は異なる周波数で送信と受 信とが同時に行える複信方式の無線送受信機に関し、さ らに詳細には送信周波数帯域と受信周波数帯域を選択で きる複信方式の無線送受信機に関する。

[0002]

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【従来の技術】この種の従来の無線送受信機は、例えば 図6に示すように構成されて、アンテナ発振・変調器2 0から出力される被変調波出力を電力増幅器21によっ て増幅し、電力増幅された被変調波出力をアンテナ15 から送信する。アンテナ15によって受信した受信波は 高周波スイッチ回路22を介して選択的にバンドパスフ ィルタ23または24の一方にに供給し、バンドパスフ ィルタ23、24からの出力は高周波スイッチ回路25 を介して取り出し、高周波増幅器26に供給して増幅 し、増幅出力はミキサ回路27に供給して局部発振器2 8からの発振出力と周波数混合し、ミキサ回路27の出 力は中間周波増幅器29に供給して増幅し、復調回路3 0に供給して復調している。高周波スイッチ回路22お よび25はスイッチ制御回路31によって送受信周波数 に基づいて制御する。

【0003】上記した、従来の無線送受信機によってf b周波数帯で受信中に f a周波数帯で送信する複信の場 合には、発振・変調器20をfa周波数帯に設定し、電 力増幅器21を経て送信する。一方、高周波スイッチ回 路22および25はバンドパスフィルタ24 (通過周波 数帯域 f b) 側に切り換え、バンドパスフィルタ24を 通過した受信信号を高周波増幅器26において増幅し、 中間周波数に周波数変換し、中間周波増幅器29によっ て増幅し、復調回路30によって復調して出力する。逆 に、fa周波数帯で受信中にfb周波数帯で送信する複 信の場合には、発振・変調器20をfb周波数帯に設定 し、電力増幅器21を経て送信する。一方、高周波スイ ッチ回路22および25はパンドパスフィルタ23(通 過周波数帯域 fa) 側に切り換え、パンドパスフィルタ 23を通過した受信信号を高周波増幅器26に導く。

[0004]

【発明が解決しようとする課題】しかしながら、上記し た従来の無線送受信機によるときは、高周波スイッチ回 10

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路のアイソレーションが悪いと送信機の出力電力が漏れ、漏れた送信出力が送信周波数帯域を通過帯域とするバンドバスフィルタを通って高周波増幅器に加わるという問題点があった。さらに、漏れが多く、このレベルが高いと、高周波増幅器およびミキサが感度抑圧を起こし、受信感度が大きく低下するという問題点が生ずる。そこで、高感度にて受信するには、高周波スイッチ回路をアイソレーションのよい回路にする必要があるが、アイソレーションのよい高周波スイッチ回路は大型化し、かつ高価になるという問題点があった。

【0005】本発明は、小型でかつ安価な構成によって、送信周波数帯域と受信周波数帯域を選択できかつ高感度の受信が可能な無線送受信機を提供することを目的とする。

#### [0006]

【課題を解決するための手段】本発明の無線送受信機 は、異なる周波数帯で送信と受信とが同時に行える複信 方式の無線送受信機において、アンテナに接続された通 過周波数帯域が異なる第1および第2のバンドパスフィ ルタと、第1のバンドパスフィルタに接続された第1の 高周波スイッチ回路および第2のバンドパスフィルタに 接続された第2の高周波スイッチ回路と、第1の高周波 スイッチ回路を通した受信信号を増幅して中間周波数に 変換する第1の受信部および第1の高周波スイッチ回路 を通した受信信号を増幅して中間周波数に変換する第2 の受信部と、第1の高周波スイッチ回路を通して第1の バンドパスフィルタの通過周波数帯域の周波数の送信を する第1の送信部および第2の高周波スイッチ回路を通 して第2のバンドパスフィルタの通過周波数帯域の周波 数の送信をする第2の送信部と、第1および第2の受信 部からの出力の一方を選択して中間周波増幅器へ出力す る選択手段と、第1のバンドパスフィルタの通過周波数 帯域の周波数の受信を行うと共に第2のバンドパスフィ ルタの通過周波数帯域の周波数による送信を行うとき、 第1の受信部を動作状態に、第2の受信部を非動作状態 に、第1の送信部を非動作状態に、第2の送信部を動作 状態に、第1の高周波スイッチ回路を第1の受信部へ受 信信号を導く側に、第2の高周波スイッチ回路を第2の 送信部の出力通過側におよび選択手段を第1の受信部か らの出力選択側に制御し、かつ第1のパンドパスフィル 夕の通過周波数帯域の周波数による送信を行うと共に第 2のパンドパスフィルタの通過周波数帯域の周波数の受 信を行うとき、第1の受信部を非動作状態に、第2の受 信部を動作状態に、第1の送信部を動作状態に、第2の 送信部を非動作状態に、第1の高周波スイッチ回路を第 1の送信部の出力通過側に、第2の高周波スイッチ回路 を第2の受信部へ受信信号を導く側におよび選択手段を 第2の受信部からの出力選択側に制御する制御手段と、 を備えたことを特徴とする。

【0007】本発明の無線送受信機において、制御手段 50 ことになる。

は、第1のパンドパスフィルタからの受信信号を第1の 受信部に導く第1のダイオードスイッチ回路と、第1の 送信部の出力を第1のパンドパスフィルタに導く第2の ダイオードスイッチ回路と、第2のパンドパスフィルタ からの受信信号を第2の受信部に導く第3のダイオード スイッチ回路と、第2の送信部の出力を第2のパンドパ スフィルタへ導く第4のダイオードスイッチ回路と、第 1の受信部の出力を選択する第5のダイオードスイッチ 回路と、第2の受信部の出力を選択する第6のダイオー ドスイッチ回路と、第1のパンドパスフィルタの通過周 波数帯域の周波数の受信を行うと共に第2のパンドパス フィルタの通過周波数帯域の周波数による送信を行う場 合に、第1、第4および第5のダイオードスイッチ回路 をオン状態に制御すると共に第1の受信部および第2の 送信部に電源を供給する第1のトランジスタスイッチ手 段と、第1のパンドパスフィルタの通過周波数帯域の周 波数による送信を行うと共に第2のバンドパスフィルタ の通過周波数帯域の周波数の受信を行う場合に、第2、 第3および第6のダイオードスイッチ回路をオン状態に 制御すると共に第2の受信部および第1の送信部に電源 を供給する第1のトランジスタスイッチ手段とを備えた ことを特徴とする。

#### [8000]

【作用】本発明の無線送受信機は、第1のバンドパスフ ィルタの通過周波数帯域の周波数の受信を行うと共に第 2のパンドパスフィルタの通過周波数帯域の周波数によ る送信を行うときには、第1のバンドパスフィルタを通 して受信した受信信号が第1の受信部において受信さ れ、第1の受信部からの出力は選択手段によって選択さ れる。一方、第2の送信部からの送信出力は第2のバン ドパスフィルタを介してアンテナから送信される。この ときに、第1、第2高周波スイッチ回路および中間周波 スイッチ回路のアイソレーションが悪くても、第2の受 信部および第1の送信部は非動作状態に制御されている ために、第2受信部および第1送信部は減衰器として作 用し、第1受信部の感度抑圧は低減されて、高感度の受 信ができることになる。第1のバンドパスフィルタの通 過周波数帯域の周波数による送信を行うと共に第2のバ ンドバスフィルタの通過周波数帯域の周波数の受信を行 うときには、第2のバンドパスフィルタを通して受信し た受信信号が第2の受信部において受信され、第2の受 信部からの出力は選択手段によって選択される。一方、 第1の送信部からの送信出力は第1のパンドパスフィル タを介してアンテナから送信される。このときに、第 1、第2高周波スイッチ回路および中間周波スイッチ回 路のアイソレーションが悪くても、第1の受信部および 第2の送信部は非動作状態に制御されているために、第 1受信部および第2送信部は減衰器として作用し、第2 受信部の感度抑圧は低減されて、髙感度の受信ができる

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[0009]

【実施例】以下、本発明を実施例により説明する。図1 は本発明の一実施例の構成を示すプロック図である。

【0010】アンテナ15にはfb周波数帯域のRF信号を通過帯域とするパンドパスフィルタ1が接続してあって、アンテナ15にて電気信号に変換されたfb周波数帯域のRF信号はパンドパスフィルタ1を通して高周波スイッチ回路2に供給し、高周波スイッチ回路2を通ったRF信号を高周波増幅器3に供給して増幅し、増幅RF信号をミキサ回路4に導いて局部発振器5の発振出10カの周波数と周波数混合して中間周波数に変換するように、fb周波数帯の受信側が構成してある。

【0011】一方、送信側については、マイクロフォンなどからの入力信号を発振・変調器9に導いて、発振・変調器9において発振される搬送波信号を入力信号によって変調し、発振・変調器9から出力される変調波信号は高周波スイッチ回路10を介して電力増幅器6に供給して、電力増幅し、増幅出力を高周波スイッチ回路2を介してバンドパスフィルタ1を通してアンテナ15から送信するように構成してある。

【0012】さらに、アンテナ15にはfa周波数帯域のRF信号を通過帯域とするバンドパスフィルタ11が接続してあって、アンテナ15にて電気信号に変換されたfa周波数帯域のRF信号はバンドパスフィルタ11を通して高周波スイッチ回路12に供給し、高周波スイッチ回路12を通ったRF信号を高周波増幅器13に供給して増幅し、増幅RF信号をミキサ回路14に導いて局部発振器5の発振出力の周波数と周波数混合して中間周波数に変換するように、fa周波数帯の受信側が構成してある。さらに、ミキサ回路4から出力される中間周波信号とは中間周波イッチ回路17に供給して一方を選択して中間周波増幅器7に供給し、増幅された中間周波信号を復調器8に供給して復調し、出力するように構成してある。

【0013】一方、発振・変調器9からの変調被信号は高周波スイッチ回路10を介して電力増幅器16に供給して、電力増幅し、増幅出力を高周波スイッチ回路12を介してパンドパスフィルタ11を通してアンテナ15から送信するように構成してある。

【0014】すなわち、本実施例の無線送受信機は、中間周波段を共通にした同様構成の一対の受信側高周波段部と、発振・変調器を共通にした同様構成の一対の送信部とで構成してある。

【0015】ここで、fb周波数帯受信、fa周波数帯送信中は、スイッチ制御回路18の制御のもとに、高周波増幅器3、ミキサ回路4および電力増幅器16を動作状態に制御し、高周波増幅器13、ミキサ回路14および電力増幅器6を非動作状態に制御し、パンドパスフィルタ1の出力を高周波増幅器3に導く状態に高周波スイ50

ッチ回路2を制御し、電力増幅器16からの出力をバンドパスフィルタ11に導く状態に高周波スイッチ回路12を制御し、発振・変調器9からの出力被変調被信号を電力増幅器16に導く状態に高周波スイッチ回路10を制御し、ミキサ回路4によって周波数変換された中間周波信号を中間周波増幅器7へ導く状態に中間周波スイッチ回路17をそれぞれ制御する。

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【0016】一方、fa周波数帯受信、fb周波数帯送信中は、スイッチ制御回路18の制御のもとに、高周波増幅器3、ミキサ回路4および電力増幅器16を非動作状態に制御し、高周波増幅器13、ミキサ回路14および電力増幅器6を動作状態に制御し、パンドパスフィルタ11の出力を高周波増幅器13に導く状態に高周波スイッチ回路12を制御し、電力増幅器6からの出力をバンドパスフィルタ1に導く状態に高周波スイッチ回路2を制御し、発振・変調器9からの出力被変調波信号を電力増幅器6に導く状態に高周波スイッチ回路10を制御し、ミキサ回路14によって周波数変換された中間周波信号を中間周波増幅器7へ導く状態に中間周波イッチ回路17をそれぞれ制御する。

【0017】高周波スイッチ回路2は、図2に示すようにダイオードD1およびD2をスイッチング素子とするダイオードスイッチ回路にて構成し、fb周波数帯受信、fa周波数帯送信中、高電位信号を制御信号として端子Aに印加してダイオードD2をオン状態に制御し、バンドパスフィルタ1からの出力を高周波増幅器3に導き、fa周波数帯受信、fb周波数帯送信中、高電位信号を制御信号として端子Bに印加してダイオードD1をオン状態に制御して電力増幅器6からの出力をパンドパスフィルタ1に導く。

【0018】高周波スイッチ回路12も図2と同様に、ダイオードD1およびD2をスイッチング素子とするダイオードスイッチ回路にて構成し、fb周波数帯受信、fa周波数帯送信中、高電位信号を制御信号として端子(A)に印加してダイオードD1をオン状態に制御し、電力増幅器16からの出力をパンドパスフィルタ11に 導き、fa周波数帯受信、fb周波数帯送信中、高電位信号を制御信号として端子(B)に印加してダイオード D2をオン状態に制御してパンドパスフィルタ11からの出力を高周波増幅器13に導く。

【0019】高周波スイッチ回路10も図2と同様に、ダイオードD1およびD2をスイッチング素子とするダイオードスイッチ回路にて構成し、fb周波数帯受信、fa周波数帯送信中、高電位信号を制御信号として端子(A)に印加してダイオードD1をオン状態に制御し、発振・変調器10からの出力変調波信号を電力増幅器16に導き、fa周波数帯受信、fb周波数帯送信中、高電位信号を制御信号として端子(B)に印加してダイオードD2をオン状態に制御して発振・変調器10からの出力変調波信号を電力増幅器6に導く。

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【0020】中間周波スイッチ回路17は、図3に示す ようにダイオードD3およびD4をスイッチング素子と するダイオードスイッチ回路にて構成し、fb周波数帯 受信、fa周波数帯送信中、高電位信号を制御信号とし て端子Aに印加してダイオードD3をオン状態に制御 し、ミキサ回路4から出力される中間周波信号を中間周 波増幅器7に導き、fa周波数帯受信、fb周波数帯送 信中、高電位信号を制御信号として端子Bに印加してダ イオードD4をオン状態に制御し、ミキサ回路14から 出力される中間周波信号を中間周波増幅器7に導く。

[0021] スイッチ制御回路18は、図4に示すよう に無線送受信機を制御するマイクロコンピュータ19か ら、fb周波数帯受信、fa周波数帯送信中のみ低電位 となる電圧がトランジスタQ1のペースに電流を流しト ランジスタQ1をオン状態に制御し、トランジスタQ1 を介した電圧を電源電圧として高周波増幅器3、ミキサ 回路4および電力増幅器16に電源電圧として印加して 動作状態に制御すると共に、高周波スイッチ回路2、1 0、12および中間周波スイッチング回路6の端子A

(A) に印加して、バンドパスフィルタ1の出力を高周 20 波増幅器3に導き、電力増幅器16からの出力をバンド パスフィルタ11に導き、発振・変調器9からの出力被 変調波信号を電力増幅器16に導き、ミキサ回路4によ って周波数変換された中間周波信号を中間周波増幅器7 へ導くように制御する。

[0022] また、スイッチ制御回路18は、fa周波 数帯受信、fb周波数帯送信中のみ低電位となる電圧が トランジスタQ2のペースに電流を流しトランジスタQ 2をオン状態に制御し、トランジスタQ2を介した電圧 を電源電圧として高周波増幅器13、ミキサ回路14お よび電力増幅器6に電源電圧として印加して動作状態に 制御すると共に、高周波スイッチ回路2、10、12お よび中間周波スイッチング回路6の端子B(B)に印加 して、バンドパスフィルタ11の出力を高周波増幅器1 3に導き、電力増幅器6からの出力をパンドパスフィル タ1に導き、発振・変調器9からの出力変調波信号を電 力増幅器6に導き、ミキサ回路14によって周波数変換 された中間周波信号を中間周波増幅器7へ導く状態に中 間周波スイッチ回路17をそれぞれ制御する。

【0023】また、パンドパスフィルタ1および11の 40 通過周波数特性は図5においてaおよびbに示す特性を 有するように構成してある。

【0024】上記のように構成された本実施例の無線送 受信機において、fb周波数帯受信、fa周波数帯送信 中は、トランジスタQ1がオン状態に制御され、トラン ジスタQ1を介した電圧が電源電圧として印加されて高 周波増幅器3、ミキサ回路4および電力増幅器16が動 作状態にされ、髙周波スイッチ回路2、10、12およ び中間周波スイッチング回路6の端子A(A)に印加さ れる。したがって、髙周波スイッチ回路2は高周波増幅 50

器3側に、高周波スイッチ回路12は電力増幅器16側 に、中間周波スイッチ回路17はミキサ回路4側に切り 換えられる。

【0025】したがって、バンドパスフィルタ1を通っ た f b 周波数帯の受信信号が高周波スイッチ回路2を通 して高周波増幅器3に導かれて、高周波増幅器3におい て増幅され、ミキサ回路4において中間周波信号に周波 数変換されて、中間周波スイッチ回路17を通して中間 周波増幅器7に導かれて増幅され、復調器8において復 調される。

【0026】一方、発振・変調器9はfa周波数周波数 帯の搬送波を発振しこれに変調をかけ、発振・変調器9 からの出力変調波信号は高周波スイッチ回路10を通し て電力増幅器16に供給され、電力増幅器16において 電力増幅され、電力増幅された送信出力は高周波スイッ チ回路12を通してバンドパスフィルタ11、アンテナ 15を介して送信される。この結果、fa周波数帯の送 信出力の送信が行われ、fb周波数帯の受信信号の受信 が行われる複信による通信がなされる。

【0027】なお、この場合は高周波増幅器13、ミキ サ回路14および電力増幅器6は動作状態に制御され ず、非動作状態にされている。したがって、高周波スイ ッチ回路2および10のアイソレションがそれほど良く なくて、高周波スイッチ回路10からの送信出力の漏れ が多いときにおいても、電力増幅器6は非動作状態に制 御されているため、周波数 f a の漏れた高周波信号が高 周波増幅器3に漏れることはなく、さらに、高周波スイ ッチ回路12のアイソレーションがそれほど良くなくて 電力増幅器16からの送信出力の漏れが多いときにおい ても、高周波増幅器13およびミキサ回路14は非動作 状態に制御されているため、周波数 f a の漏れた送信出 力が中間周波スイッチ回路17に漏れることはなく、中 間周波スイッチ回路17のアイソレーションがそれほど 良くなくても、ミキサ回路4側に漏れることはない。

【0028】すなわち、高周波増幅器3およびミキサ回 路4において感度抑圧を起こすことはなく、非動作状態 となっている電力増幅器6、高周波増幅器13およびミ キサ回路14が減衰器として作用し、漏れ送信出力を小 さくするため、この結果、感度抑圧は小さく、fb周波 数帯受信が高感度の受信ができる。

【0029】また、逆にfb周波数帯送信、fa周波数 帯受信中は、トランジスタQ2がオン状態に制御され、 トランジスタQ2を介した電圧が電源電圧として印加さ れて高周波増幅器13、ミキサ回路14および電力増幅 器6が動作状態にされ、高周波スイッチ回路2、10、 12および中間周波スイッチング回路6の端子B(B) に印加される。したがって、高周波スイッチ回路2は高 周波増幅器13側に、高周波スイッチ回路12は電力増 幅器6側に、中間周波スイッチ回路17はミキサ回路1 4側に切り換えられる。

【0030】したがって、バンドパスフィルタ11を通ったfa周波数帯の受信信号が高周波スイッチ回路12を通して高周波増幅器13に導かれて、高周波増幅器13において増幅され、ミキサ回路14において中間周波信号に周波数変換されて、中間周波スイッチ回路17を通して中間周波増幅器7に導かれて増幅され、復調器8において復調される。

【0031】一方、発振・変調器9はfb周波数周波数帯の搬送波を発振しこれに変調をかけ、発振・変調器9からの出力変調波信号は高周波スイッチ回路10を通し 10 て電力増幅器6に供給され、電力増幅器6において電力増幅され、電力増幅された送信出力は高周波スイッチ回路2を通してバンドパスフィルタ1、アンテナ15を介して送信される。この結果、fb周波数帯の送信出力の送信が行われ、fa周波数帯の受信信号の受信が行われる複信による通信がなされる。

【0032】なお、この場合は高周波増幅器3、ミキサ 回路4および電力増幅器16は動作状態に制御されず、 非動作状態にされている。したがって、この場合におい て高周波スイッチ回路12および10のアイソレーショ 20 ンがそれほど良くなくて、高周波スイッチ回路12から の送信出力の漏れが多いときにおいても、電力増幅器 1 6 は非動作状態に制御されているため、周波数 f b の漏 れた高周波信号出力が高周波増幅器13に漏れることは なく、さらに、高周波スイッチ回路2のアイソレーショ ンがそれほど良くなくて電力増幅器6からの送信出力の 漏れが多いときにおいても、高周波増幅器3およびミキ サ回路4は非動作状態に制御されているため、周波数 f bの漏れた送信出力が中間周波スイッチ回路17に漏れ ることはなく、中間周波スイッチ回路17のアイソレー 30 ションがそれほど良くなくても、ミキサ回路14側に漏 れることはない。

【0033】すなわち、高周波増幅器13およびミキサ回路14において感度抑圧を起こすことはなく、非動作状態となっている電力増幅器16、高周波増幅器3およびミキサ回路4が減衰器として作用し、漏れ送信出力を小さくするため、この結果、感度抑圧は小さく、fa周波数帯受信が高感度の受信ができる。上記のように2つの異なる周波数での複信通信が高感度で行えると共に、送信周波数と受信周波数とを選択することができる。

【0034】また、高周波増幅器3、ミキサ回路4および電力増幅器6、高周波増幅器13、ミキサ回路14および電力増幅器16のそれぞれをさらに細かくオン・オフ制御することによって、単信通信がfa周波数帯、fb周波数帯で行え、さらに、fa周波数帯とfb周波数帯とを使用した半複信通信を行うこともできる。すなわ

10 ち、fa周波数帯とfb周波数帯とによって単信通信、 半複信通信、複信通信を選択することもできる。

【0035】さらに、パンドパスフィルタ、高周波増幅器、ミキサ回路および電力増幅器の数を増加し、高周波スイッチ回路および中間周波スイッチ回路による選択数を増加することによって多数の周波数を選択することもできる。

#### [0036]

【発明の効果】以上説明したように本発明によれば、受信していない方の受信部および送信していない方の送信部は非動作状態にされているため、第1、第2高周波スイッチ回路および中間周波スイッチ回路のアイソレーションが悪くても、非動作状態にされている受信部および送信部が減衰器として作用し、動作状態中の受信部への送信出力の漏れはきわめて少なくなる効果がある。また、このために動作中の信部漏れの影響を受けず、感度な受信ができることになる効果がある。

【0037】また、制御手段はダイオードスイッチ回路 およびトランジスタスイッチ手段で構成できるため、構 成が簡単ですむという効果もある。

#### 【図面の簡単な説明】

【図1】本発明の一実施例の無線送受信機の構成を示すブロック図である。

【図2】本発明の一実施例の無線送受信機における高周 波スイッチ回路の構成を示す回路図である。

【図3】本発明の一実施例の無線送受信機における中間 周波スイッチ回路の構成を示す回路図である。

【図4】本発明の一実施例の無線送受信機におけるスイッチ制御回路の構成を示すプロック図である。

) 【図5】本発明の一実施例の無線送受信機におけるバンドパスフィルタの周波数特性図である。

【図6】従来例の無線送受信機の構成を示すブロック図 である。

#### 【符号の説明】

1および11 バンドパスフィルタ

2、10および12 高周波スイッチ回路

3および13 高周波増幅器

4および14 ミキサ回路

5 局部発振器

0 6 および 1 6 電力増幅器

7 中間周波増幅器

8 復調器

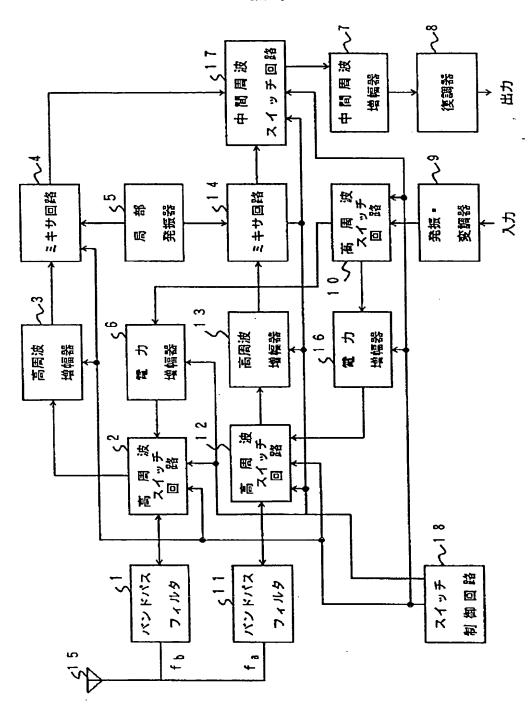
9 発振・変調器

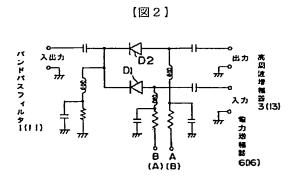
17 中間周波スイッチ回路

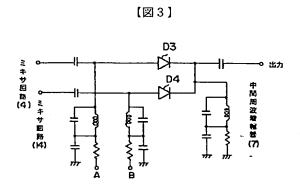
18 スイッチ制御回路

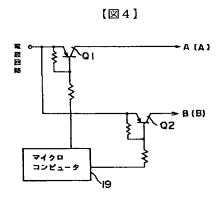
19 マイクロコンピュータ

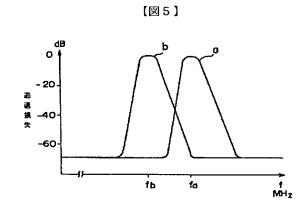
【図1】



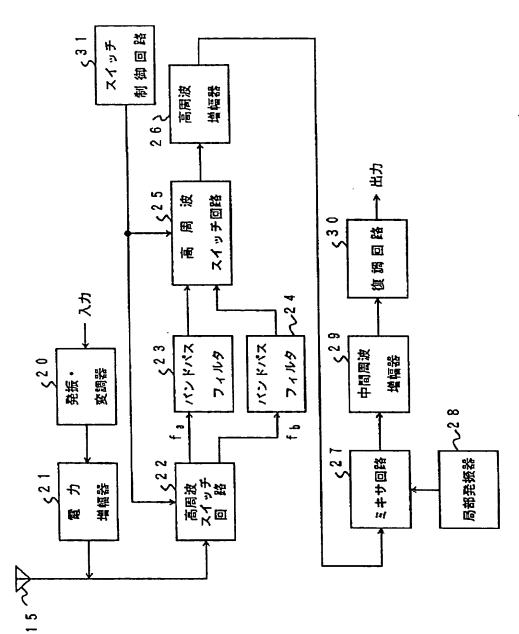












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#### CLAIMS

#### [Claim(s)]

[Claim 1] In the radio receiver-transmitter of the duplex operation which can perform transmission and reception simultaneously with a different frequency band The 1st and 2nd band pass filters with which the passage frequency bands connected to the antenna differ, The 2nd high frequency switching circuit connected to the 1st high frequency switching circuit and 2nd band pass filter which were connected to the 1st band pass filter, With the 2nd receive section which amplifies the input signal which let the 1st receive section which amplifies the input signal which let the 1st high frequency switching circuit pass, and changes into an intermediate frequency, and the 1st high frequency switching circuit pass, and changes into an intermediate frequency The 2nd transmitting section which transmits the frequency of the passage frequency band of the 2nd band pass filter through the 1st transmitting section which transmits the frequency of the passage frequency band of the 1st band pass filter through the 1st high frequency switching circuit, and the 2nd high frequency switching circuit, A selection means to choose one side of the output from the 1st and 2nd receive sections, and to output to the intermediate frequency amplifier, While receiving the frequency of the passage frequency band of the 1st band pass filter, when performing transmission by the frequency of the passage frequency band of the 2nd band pass filter, To operating state, the 1st transmitting section for the 2nd receive section at non-operating state to non-operating state [ the 1st receive section ] To operating state, the 2nd transmitting section at the side which draws an input signal for the 1st high frequency switching circuit to the 1st receive section A selection means is controlled for the 2nd high frequency switching circuit to the output selection side from the 1st receive section in the output passage side of the 2nd transmitting section. And while performing transmission by the frequency of the passage frequency band

of the 1st band pass filter, when receiving the frequency of the passage frequency band of the 2nd band pass filter, The 1st transmitting section for the 2nd receive section to non-operating state at operating state to operating state [ the 1st receive section ] The 1st high frequency switching circuit to non-operating state for the 2nd transmitting section to the output passage side of the 1st transmitting section The radio receiver-transmitter characterized by having the control means which controls a selection means to the output selection side from the 2nd receive section for the 2nd high frequency switching circuit.

[Claim 2] In a radio receiver-transmitter according to claim 1 a control means The 1st diode switch circuit which leads the input signal from the 1st band pass filter to the 1st receive section, The 2nd diode switch circuit which leads the output of the 1st transmitting section to the 1st band pass filter, The 3rd diode switch circuit which leads the input signal from the 2nd band pass filter to the 2nd receive section, The 4th diode switch circuit which leads the output of the 2nd transmitting section to the 2nd band pass filter, The 5th diode switch circuit which chooses the output of the 1st receive section, and the 6th diode switch circuit which chooses the output of the 2nd receive section, While receiving the frequency of the passage frequency band of the 1st band pass filter The 1st transistor switching means which supplies a power source to the 1st receive section and the 2nd transmitting section while controlling the 1st, 4th, and 5th diode switch circuits to an ON state, when performing transmission by the frequency of the passage frequency band of the 2nd band pass filter, While performing transmission by the frequency of the passage frequency band of the 1st band pass filter When receiving the frequency of the passage frequency band of the 2nd band pass filter, while controlling the 2nd, 3rd, and 6th diode switch circuits to an ON state The radio receiver-transmitter characterized by having the 1st transistor switching means which supplies a power source to the 2nd receive section and the 1st transmitting section.

[Translation done.]

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#### DETAILED DESCRIPTION

[Detailed Description of the Invention] [0001]

[Industrial Application] This invention relates to the radio receiver—transmitter of the duplex operation which can choose a transmit—frequencies band and a received frequency band as a detail further about the radio receiver—transmitter of the duplex operation which can perform transmission and reception simultaneously on a different frequency.
[0002]

[Description of the Prior Art] This kind of conventional radio receivertransmitter is constituted as shown in drawing 6, amplifies the modulated wave output outputted from an antenna oscillation and a modulator 20 with power amplifier 21, and transmits the modulated wave output by which power amplification was carried out from an antenna 15. the received wave received with the antenna 15 was selectively boiled and supplied to one side of band pass filters 23 or 24 through the highfrequency switching circuit 22, the output from band pass filters 23 and 24 supplied and amplified to ejection and the high-frequency amplifier 26 through the high-frequency switching circuit 25, the magnification output supplied to the mixer circuit 27, and carried out frequencymixing to the oscillation output from a local oscillator 28, and it supplies and amplifies to the intermediate frequency amplifier 29, and the output of a mixer circuit 27 supplied to the demodulator circuit 30, and it has restored to it. The high frequency switching circuits 22 and 25 are controlled by the switch control circuit 31 based on a transceiver frequency.

[0003] In the case of the duplex operation which transmits with fa frequency band during reception with fb frequency band, an oscillation and a modulator 20 are set up with the above-mentioned conventional radio receiver-transmitter at fa frequency band, and it transmits through power amplifier 21. On the other hand, the high frequency switching circuits 22 and 25 amplify the input signal which passed the switch and the band pass filter 24 in the high-frequency amplifier 26 to a band pass filter 24 (passage frequency band fb) side, it carries out frequency conversion to an intermediate frequency, and they amplify it with the intermediate frequency amplifier 29, and are restored to them

and outputted by the demodulator circuit 30. On the contrary, in the case of the duplex operation which transmits with fb frequency band during reception with fa frequency band, an oscillation and a modulator 20 are set as fb frequency band, and it transmits through power amplifier 21. On the other hand, the high frequency switching circuits 22 and 25 lead the input signal which passed the switch and the band pass filter 23 to the band pass filter 23 (passage frequency band fa) side to the high-frequency amplifier 26.

[0004]

[Problem(s) to be Solved by the Invention] However, when were based on the above-mentioned conventional radio receiver-transmitter, and the isolation of a high frequency switching circuit was bad, there was a trouble that the output power of a transmitter joined the high-frequency amplifier through the band pass filter with which leakage and the transmitting output which leaked make a transmit-frequencies band a passband. Furthermore, if there is much leakage and this level is high, the trouble that a lifting and receiving sensibility fall [ the high-frequency amplifier and a mixer ] desensitization greatly will arise. Then, although the high frequency switching circuit needed to be made into the good circuit of an isolation in order to have received in high sensitivity, the good high frequency switching circuit of an isolation was enlarged, and had the trouble of becoming expensive.

[0005] This invention aims at being able to choose a transmit-

[0005] This invention aims at being able to choose a transmit-frequencies band and a received frequency band, and offering the radio receiver-transmitter which can receive high sensitivity by the small and cheap configuration.

[0006]

[Means for Solving the Problem] In the radio receiver-transmitter of the duplex operation which can perform transmission and reception simultaneously with the frequency band with which the radios receiver-transmitter of this invention differ The 1st and 2nd band pass filters with which the passage frequency bands connected to the antenna differ, The 2nd high frequency switching circuit connected to the 1st high frequency switching circuit and 2nd band pass filter which were connected to the 1st band pass filter, With the 2nd receive section which amplifies the input signal which let the 1st receive section which amplifies the input signal which let the 1st high frequency switching circuit pass, and changes into an intermediate frequency, and the 1st high frequency switching circuit pass, and changes into an intermediate frequency The 2nd transmitting section which transmits the frequency of the passage frequency band of the 2nd band pass filter through the 1st

transmitting section which transmits the frequency of the passage frequency band of the 1st band pass filter through the 1st high frequency switching circuit, and the 2nd high frequency switching circuit, A selection means to choose one side of the output from the 1st and 2nd receive sections, and to output to the intermediate frequency amplifier, While receiving the frequency of the passage frequency band of the 1st band pass filter, when performing transmission by the frequency of the passage frequency band of the 2nd band pass filter, To operating state, the 1st transmitting section for the 2nd receive section at non-operating state to non-operating state [ the 1st receive section ] To operating state, the 2nd transmitting section at the side which draws an input signal for the 1st high frequency switching circuit to the 1st receive section A selection means is controlled for the 2nd high frequency switching circuit to the output selection side from the 1st receive section in the output passage side of the 2nd transmitting section. And while performing transmission by the frequency of the passage frequency band of the 1st band pass filter, when receiving the frequency of the passage frequency band of the 2nd band pass filter, The 1st transmitting section for the 2nd receive section to non-operating state at operating state to operating state [ the 1st receive section ] It is characterized by equipping non-operating state with the control means which controls a selection means to the output selection side from the 2nd receive section side it leads [ the 1st high frequency switching circuit ] an input signal to the 2nd receive section for the 2nd high frequency switching circuit at the output passage side of the 1st transmitting section for the 2nd transmitting section. [0007] In the radio receiver-transmitter of this invention a control means The 1st diode switch circuit which leads the input signal from the 1st band pass filter to the 1st receive section, The 2nd diode switch circuit which leads the output of the 1st transmitting section to the 1st band pass filter, The 3rd diode switch circuit which leads the input signal from the 2nd band pass filter to the 2nd receive section, The 4th diode switch circuit which leads the output of the 2nd transmitting section to the 2nd band pass filter, The 5th diode switch circuit which chooses the output of the 1st receive section, and the 6th diode switch circuit which chooses the output of the 2nd receive section, While receiving the frequency of the passage frequency band of the 1st band pass filter, when performing transmission by the frequency of the passage frequency band of the 2nd band pass filter The 1st transistor switching means which supplies a power source to the 1st receive section and the 2nd transmitting section while controlling the 1st, 4th, and 5th diode switch circuits to an ON state, While performing transmission by the frequency of the passage frequency band of the 1st band pass filter, when receiving the frequency of the passage frequency band of the 2nd band pass filter While controlling the 2nd, 3rd, and 6th diode switch circuits to an ON state, it is characterized by having the 1st transistor switching means which supplies a power source to the 2nd receive section and the 1st transmitting section.

[0008]

[Function] While the radio receiver-transmitter of this invention receives the frequency of the passage frequency band of the 1st band pass filter, when performing transmission by the frequency of the passage frequency band of the 2nd band pass filter, the input signal which received through the 1st band pass filter is received in the 1st receive section, and the output from the 1st receive section is chosen by the selection means. On the other hand, the transmitting output from the 2nd transmitting section is transmitted from an antenna through the 2nd band pass filter. Since the 2nd receive section and the 1st transmitting section are controlled by non-operating state at this time even if the isolation of the 1st and 2nd high frequency switching circuit and an intermediate frequency switching circuit is bad, the 2nd receive section and the 1st transmitting section act as an attenuator, and the desensitization of the 1st receive section will be reduced and can perform reception of high sensitivity. While performing transmission by the frequency of the passage frequency band of the 1st band pass filter, when receiving the frequency of the passage frequency band of the 2nd band pass filter, the input signal which received through the 2nd band pass filter is received in the 2nd receive section, and the output from the 2nd receive section is chosen by the selection means. On the other hand, the transmitting output from the 1st transmitting section is transmitted from an antenna through the 1st band pass filter. Since the 1st receive section and the 2nd transmitting section are controlled by non-operating state at this time even if the isolation of the 1st and 2nd high frequency switching circuit and an intermediate frequency switching circuit is bad, the 1st receive section and the 2nd transmitting section act as an attenuator, and the desensitization of the 2nd receive section will be reduced and can perform reception of high sensitivity.

[0009]

[Example] Hereafter, an example explains this invention. Drawing 1 is the block diagram showing the configuration of one example of this invention. [0010] The band pass filter 1 which makes the RF signal of fb frequency band a passband is connected to the antenna 15. The RF signal of fb frequency band changed into the electrical signal with the antenna 15 is supplied to the high frequency switching circuit 2 through a band pass filter 1. The receiving side of fb frequency band is constituted so that may supply and amplify the RF signal passing through the high frequency switching circuit 2 to the high-frequency amplifier 3, a magnification RF signal may be led to a mixer circuit 4, frequency-mixing may be carried out to the frequency of the oscillation output of a local oscillator 5 and it may change into an intermediate frequency. [0011] On the other hand, the input signal from a microphone etc. is led to an oscillation and a modulator 9, the carrier signal oscillated in an oscillation and a modulator 9 is modulated with an input signal, and power amplification of the modulated wave signal outputted from an oscillation and a modulator 9 is supplied and carried out to power amplifier 6 through the high frequency switching circuit 10, and the transmitting side constitutes it so that a magnification output may be transmitted from an antenna 15 through a band pass filter 1 through the high frequency switching circuit 2.

[0012] Furthermore, the band pass filter 11 which makes the RF signal of fa frequency band a passband is connected to the antenna 15. The RF signal of fa frequency band changed into the electrical signal with the antenna 15 is supplied to the high frequency switching circuit 12 through a band pass filter 11. The receiving side of fa frequency band is constituted so that may supply and amplify the RF signal passing through the high frequency switching circuit 12 to the high-frequency amplifier 13, a magnification RF signal may be led to a mixer circuit 14, frequency-mixing may be carried out to the frequency of the oscillation output of a local oscillator 5 and it may change into an intermediate frequency. Furthermore, the intermediate frequency signal outputted from a mixer circuit 4 and the intermediate frequency signal outputted from a mixer circuit 14 are supplied to the intermediate frequency switching circuit 17, and chooses one side, it is supplied to the intermediate frequency amplifier 7, and it constitutes it so that the amplified intermediate frequency signal may be supplied, restored to it and outputted to a demodulator 8.

[0013] On the other hand, power amplification of the modulated wave signal from an oscillation and a modulator 9 is supplied and carried out to power amplifier 16 through the high frequency switching circuit 10, and it is constituted so that a magnification output may be transmitted from an antenna 15 through a band pass filter 11 through the high

frequency switching circuit 12.

[0014] that is, the radio receiver-transmitter of this example carried out the intermediate frequency stage in common -- the oscillation and the modulator were similarly carried out to the receiving-side RF step of the couple of a configuration in common -- it constitutes from the transmitting section of the couple of a configuration similarly. [0015] Here during fb frequency band reception and fa frequency band transmission The high-frequency amplifier 3, a mixer circuit 4, and power amplifier 16 are controlled to operating state at the basis of control of the switch control circuit 18. The high-frequency amplifier 13, a mixer circuit 14, and power amplifier 6 are controlled to nonoperating state. The high frequency switching circuit 2 is controlled in the condition of leading the output of a band pass filter 1 to a highfrequency amplifier 3. The high frequency switching circuit 12 is controlled in the condition of leading the output from power amplifier 16 to a band pass filter 11. The high frequency switching circuit 10 is controlled in the condition of leading the output modulated wave signal from an oscillation and a modulator 9 to power amplifier 16, and the intermediate frequency switching circuit 17 is controlled, respectively in the condition of leading the intermediate frequency signal by which frequency conversion was carried out in the mixer circuit 4 to the intermediate frequency amplifier 7.

[0016] On the other hand, during fa frequency band reception and fb frequency band transmission The high-frequency amplifier 3, a mixer circuit 4, and power amplifier 16 are controlled to non-operating state at the basis of control of the switch control circuit 18. The highfrequency amplifier 13, a mixer circuit 14, and power amplifier 6 are controlled to operating state. The high frequency switching circuit 12 is controlled in the condition of leading the output of a band pass filter 11 to a high-frequency amplifier 13. The high frequency switching circuit 2 is controlled in the condition of leading the output from power amplifier 6 to a band pass filter 1. The high frequency switching circuit 10 is controlled in the condition of leading the output modulated wave signal from an oscillation and a modulator 9 to power amplifier 6, and the intermediate frequency switching circuit 17 is controlled, respectively in the condition of leading the intermediate frequency signal by which frequency conversion was carried out in the mixer circuit 14 to the intermediate frequency amplifier 7. [0017] The high frequency switching circuit 2 constitutes diodes D1 and D2 from a diode switch circuit used as a switching element, as shown in

drawing 2. During fb frequency band reception and fa frequency band

transmission, it is impressed by Terminal A by making a high potential signal into a control signal, and diode D2 is controlled to an ON state. The output from a band pass filter 1 is led to the high-frequency amplifier 3, during fa frequency band reception and fb frequency band transmission, it is impressed by Terminal B by making a high potential signal into a control signal, diode D1 is controlled to an ON state, and the output from power amplifier 6 is led to a band pass filter 1. [0018] The high frequency switching circuit 12 as well as drawing 2 is constituted from a diode switch circuit which uses diodes D1 and D2 as a switching element. During fb frequency band reception and fa frequency band transmission, it is impressed by the terminal (A) by making a high potential signal into a control signal, and diode D1 is controlled to an ON state. The output from power amplifier 16 is led to a band pass filter 11, during fa frequency band reception and fb frequency band transmission, it is impressed by the terminal (B) by making a high potential signal into a control signal, diode D2 is controlled to an ON state, and the output from a band pass filter 11 is led to the highfrequency amplifier 13.

[0019] The high frequency switching circuit 10 as well as drawing 2 is constituted from a diode switch circuit which uses diodes D1 and D2 as a switching element. During fb frequency band reception and fa frequency band transmission, it is impressed by the terminal (A) by making a high potential signal into a control signal, and diode D1 is controlled to an ON state. The output modulated wave signal from an oscillation and a modulator 10 is led to power amplifier 16, during fa frequency band reception and fb frequency band transmission, it is impressed by the terminal (B) by making a high potential signal into a control signal, diode D2 is controlled to an ON state, and the output modulated wave signal from an oscillation and a modulator 10 is led to power amplifier 6.

[0020] The intermediate frequency switching circuit 17 constitutes diodes D3 and D4 from a diode switch circuit used as a switching element, as shown in drawing 3. During fb frequency band reception and fa frequency band transmission, it is impressed by Terminal A by making a high potential signal into a control signal, and diode D3 is controlled to an ON state. The intermediate frequency signal outputted from a mixer circuit 4 is led to the intermediate frequency amplifier 7. During fa frequency band reception and fb frequency band transmission, it is impressed by Terminal B by making a high potential signal into a control signal, diode D4 is controlled to an ON state, and the intermediate frequency signal outputted from a mixer circuit 14 is led to the

intermediate frequency amplifier 7.

[0021] The switch control circuit 18 from the microcomputer 19 which controls a radio receiver-transmitter to be shown in drawing 4 The electrical potential difference which serves as low voltage only during fb frequency band reception and fa frequency band transmission controls a current at the base of a transistor Q1, and controls the sink transistor Q1 to an ON state. While being impressed by the highfrequency amplifier 3, the mixer circuit 4, and power amplifier 16 as supply voltage by making the electrical potential difference through a transistor Q1 into supply voltage and controlling to operating state It is impressed by terminal [ of the high frequency switching circuits 2, 10, and 12 and the intermediate frequency switching circuit 6 ] A (A). Lead the output of a band pass filter 1 to a high-frequency amplifier 3, and the output from power amplifier 16 is led to a band pass filter 11. It controls to lead the output modulated wave signal from an oscillation and a modulator 9 to power amplifier 16, and to lead the intermediate frequency signal by which frequency conversion was carried out in the mixer circuit 4 to the intermediate frequency amplifier 7. [0022] Moreover, the electrical potential difference from which the switch control circuit 18 serves as low voltage only during fa frequency band reception and fb frequency band transmission controls a current at the base of a transistor Q2, and controls the sink transistor Q2 to an ON state. While being impressed by the high-frequency amplifier 13, the mixer circuit 14, and power amplifier 6 as supply voltage by making the electrical potential difference through a transistor Q2 into supply voltage and controlling to operating state It is impressed by terminal [ of the high frequency switching circuits 2, 10, and 12 and the intermediate frequency switching circuit 6 ] B (B). Lead the output of a band pass filter 11 to a high-frequency amplifier 13, and the output from power amplifier 6 is led to a band pass filter 1. The intermediate frequency switching circuit 17 is controlled, respectively in the condition of leading the output modulated wave signal from an oscillation and a modulator 9 to power amplifier 6, and leading the intermediate frequency signal by which frequency conversion was carried out in the mixer circuit 14 to the intermediate frequency amplifier 7. [0023] Moreover, the passage frequency characteristics of band pass filters 1 and 11 are constituted so that it may have the property shown in a and b in drawing 5.

[0024] In the radio receiver-transmitter of this example constituted as mentioned above, during fb frequency band reception and fa frequency band transmission, a transistor Q1 is controlled by the ON state, and

the electrical potential difference through a transistor Q1 is impressed as supply voltage, and a high-frequency amplifier 3, a mixer circuit 4, and power amplifier 16 are made into operating state, and are impressed to terminal [ of the high frequency switching circuits 2, 10, and 12 and the intermediate frequency switching circuit 6 ] A (A). Therefore, the high frequency switching circuit 12 is switched to a power amplifier 16 side, and the intermediate frequency switching circuit 17 is switched for the high frequency switching circuit 2 to a high-frequency amplifier 3 side at a mixer circuit 4 side.

[0025] Therefore, it is led to the high-frequency amplifier 3 through the high frequency switching circuit 2, and is amplified in the high-frequency amplifier 3, and frequency conversion is carried out to an intermediate frequency signal in a mixer circuit 4, and through the intermediate frequency switching circuit 17, the input signal of fb frequency band which passed along the band pass filter 1 is led to the intermediate frequency amplifier 7, is amplified, and gets over in a demodulator 8.

[0026] On the other hand, an oscillation and a modulator 9 oscillate the subcarrier of fa frequency frequency band, and applies a modulation to this, the output modulated wave signal from an oscillation and a modulator 9 is supplied to power amplifier 16 through the high frequency switching circuit 10, power amplification is carried out in power amplifier 16, and the transmitting output by which power amplification was carried out is transmitted through a band pass filter 11 and an antenna 15 through the high frequency switching circuit 12. Consequently, the communication link by the duplex operation to which transmission of the transmitting output of fa frequency band is performed, and reception of the input signal of fb frequency band is performed is made. [0027] In addition, in this case, the high-frequency amplifier 13, a mixer circuit 14, and power amplifier 6 are not controlled by operating state, but are made into non-operating state. Therefore, since power amplifier 6 is controlled by non-operating state when eye SORESHON of the high frequency switching circuits 2 and 10 is not so good and there is much leakage of the transmitting output from the high frequency switching circuit 10, The high frequency signal with which the frequency fa leaked does not leak to a high-frequency amplifier 3, and when the isolation of the high frequency switching circuit 12 is not still so better and there is much leakage of the transmitting output from power amplifier 16, it also sets. Since the high-frequency amplifier 13 and a mixer circuit 14 are controlled by non-operating state, Even if the transmitting output from which the frequency fa leaked does not leak to

the intermediate frequency switching circuit 17 and the isolation of the intermediate frequency switching circuit 17 is not so good, it does not leak to a mixer circuit 4 side.

[0028] That is, since the power amplifier 6, the high-frequency amplifier 13, and mixer circuit 14 which do not start desensitization in the high-frequency amplifier 3 and a mixer circuit 4, and have become non-operating state act as an attenuator and make a leakage transmitting output small consequently, desensitization is small and reception of high sensitivity can perform fb frequency band reception.

[0029] Moreover, during fb frequency band transmission and fa frequency band reception, a transistor Q2 is controlled by the ON state, and the electrical potential difference through a transistor Q2 is impressed as supply voltage, and the high-frequency amplifier 13, a mixer circuit 14, and power amplifier 6 are made into operating state, and are impressed to reverse at terminal [ of the high frequency switching circuits 2, 10, and 12 and the intermediate frequency switching circuit 6 ] B (B). Therefore, the high frequency switching circuit 12 is switched to a power amplifier 6 side, and the intermediate frequency switching circuit 17 is switched for the high frequency switching circuit 2 to a high-frequency amplifier 13 side at a mixer circuit 14 side.

[0030] Therefore, it is led to the high-frequency amplifier 13 through the high frequency switching circuit 12, and is amplified in the high-frequency amplifier 13, and frequency conversion is carried out to an intermediate frequency signal in a mixer circuit 14, and through the intermediate frequency switching circuit 17, the input signal of fa frequency band which passed along the band pass filter 11 is led to the intermediate frequency amplifier 7, is amplified, and gets over in a demodulator 8.

[0031] On the other hand, an oscillation and a modulator 9 oscillate the subcarrier of fb frequency frequency band, and applies a modulation to this, the output modulated wave signal from an oscillation and a modulator 9 is supplied to power amplifier 6 through the high frequency switching circuit 10, power amplification is carried out in power amplifier 6, and the transmitting output by which power amplification was carried out is transmitted through a band pass filter 1 and an antenna 15 through the high frequency switching circuit 2. Consequently, the communication link by the duplex operation to which transmission of the transmitting output of fb frequency band is performed, and reception of the input signal of fa frequency band is performed is made.

[0032] In addition, in this case, the high-frequency amplifier 3, a mixer circuit 4, and power amplifier 16 are not controlled by operating

state, but are made into non-operating state. Therefore, when the isolation of the high frequency switching circuits 12 and 10 is not so good in this case and there is much leakage of the transmitting output from the high frequency switching circuit 12, it also sets. Since power amplifier 16 is controlled by non-operating state, the RF signal output from which the frequency fb leaked does not leak to the high-frequency amplifier 13. Furthermore, since the high-frequency amplifier 3 and a mixer circuit 4 are controlled by non-operating state when the isolation of the high frequency switching circuit 2 is not so good and there is much leakage of the transmitting output from power amplifier 6, Even if the transmitting output from which the frequency fb leaked does not leak to the intermediate frequency switching circuit 17 and the isolation of the intermediate frequency switching circuit 17 is not so good, it does not leak to a mixer circuit 14 side.

[0033] That is, since the power amplifier 16, the high-frequency amplifier 3, and mixer circuit 4 which do not start desensitization in the high-frequency amplifier 13 and a mixer circuit 14, and have become non-operating state act as an attenuator and make a leakage transmitting output small consequently, desensitization is small and reception of high sensitivity can perform fa frequency band reception. While a duplex operation communication link on two different frequencies can carry out by high sensitivity as mentioned above, transmit frequencies and received frequency can be chosen.

[0034] Moreover, by carrying out on-off control of each of the high-frequency amplifier 3, a mixer circuit 4 and power amplifier 6, the high-frequency amplifier 13, a mixer circuit 14, and power amplifier 16 still more finely, simplex communication can carry out with fa frequency band and fb frequency band, and can also perform further the semi-duplex operation communication link which used fa frequency band and fb frequency band. That is, simplex communication, a semi-duplex operation communication link, and a duplex operation communication link can also be chosen with fa frequency band and fb frequency band.

[0035] Furthermore, many frequencies can also be chosen by increasing the number of a band pass filter, a high-frequency amplifier, a mixer circuit, and power amplifier, and increasing the number of selections by the high frequency switching circuit and the intermediate frequency switching circuit.

[0036]

[Effect of the Invention] Since the receive section of the direction which has not received, and the transmitting section of the direction which has not transmitted are made into non-operating state according to

this invention as explained above, even if the isolation of the 1st and 2nd high frequency switching circuit and an intermediate frequency switching circuit is bad, the receive section and the transmitting section which are made into non-operating state act as an attenuator, and the leakage of the transmitting output to the receive section in operating state has the effectiveness which decreases extremely.

moreover — for this reason, it is not influenced of working \*\*\*\* leakage, but there is effectiveness which can perform sensibility reception.

[0037] Moreover, since a control means can be constituted from a diode switch circuit and a transistor switching means, it is effective in a configuration being easy and ending.

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#### DESCRIPTION OF DRAWINGS

[Brief Description of the Drawings]

[Drawing 1] It is the block diagram showing the configuration of the radio receiver-transmitter of one example of this invention.

[Drawing 2] It is the circuit diagram showing the configuration of the high frequency switching circuit in the radio receiver-transmitter of one example of this invention.

[Drawing 3] It is the circuit diagram showing the configuration of the intermediate frequency switching circuit in the radio receivertransmitter of one example of this invention.

[Drawing 4] It is the block diagram showing the configuration of the switch control circuit in the radio receiver-transmitter of one example of this invention.

[Drawing 5] It is frequency-characteristics drawing of the band pass filter in the radio receiver-transmitter of one example of this

## invention.

[Drawing 6] It is the block diagram showing the configuration of the radio receiver-transmitter of the conventional example.

# [Description of Notations]

- 1 and 11 Band pass filter
- 2, 10, and 12 High frequency switching circuit
- 3 and 13 High-frequency amplifier
- 4 and 14 Mixer circuit
- 5 Local Oscillator
- 6 and 16 Power amplifier
- 7 Intermediate Frequency Amplifier
- 8 Demodulator
- 9 Oscillation and Modulator
- 17 Intermediate Frequency Switching Circuit
- 18 Switch Control Circuit
- 19 Microcomputer

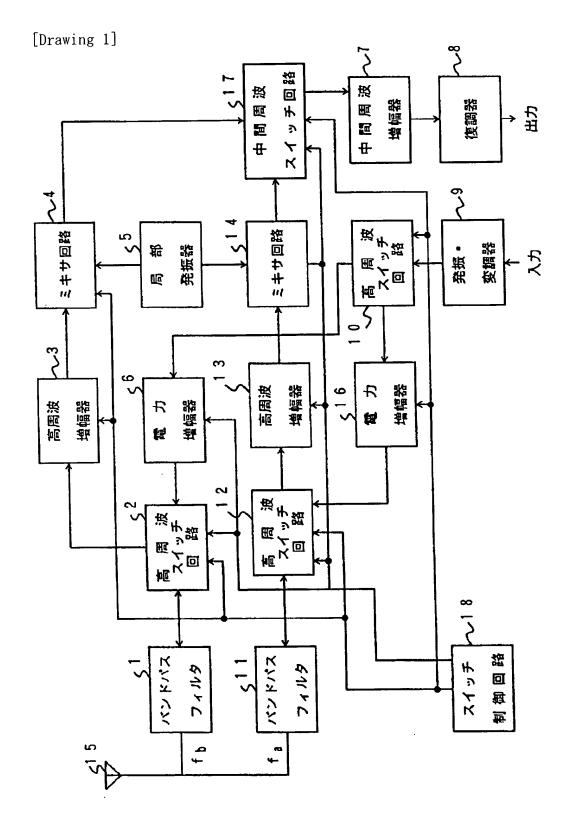
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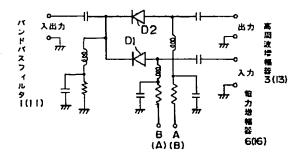
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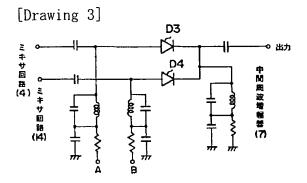
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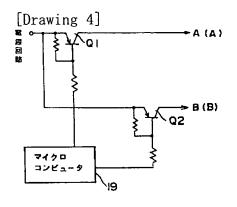
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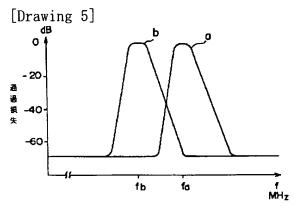


[Drawing 2]

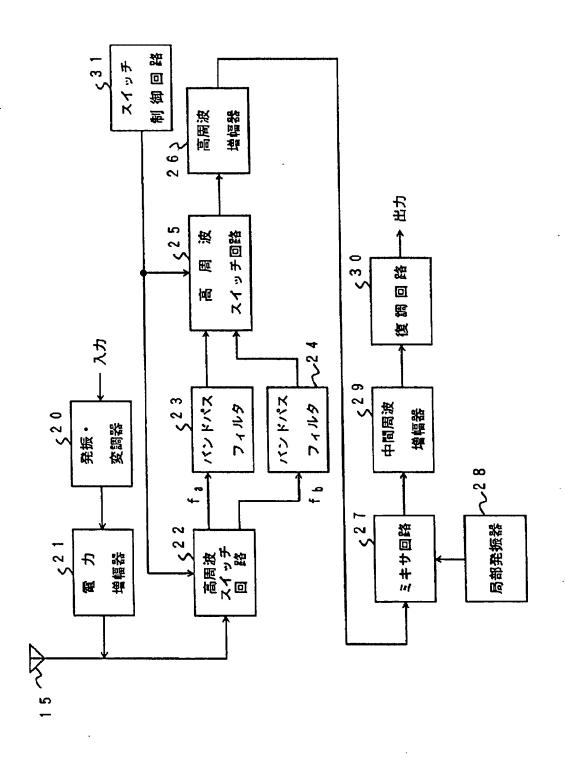








[Drawing 6]



[Translation done.]